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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/608,580	06/30/2000	Frankie F Roohparvar	400.006US01	9345

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EXAMINER

PHAN, TRONG Q

ART UNIT

PAPER NUMBER

2818

DATE MAILED: 01/08/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.
09/608,580

Applicant(s)
ROOHPARVAR

Examiner
TRONG PHAN

Art Unit
2818



-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE three MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on Jun 30, 2000.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above, claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-27 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claims _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- 13) ☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- a) ☐ All b) ☐ Some* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- *See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892) 18) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 16) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 19) ☐ Notice of Informal Patent Application (PTO-152)
- 17) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s). 2 20) ☐ Other:

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Drawings

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the features as recited in claims 16-17, 20, 23 and 25-26 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Claim Rejections - 35 USC § 112

2. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

3. Claims 1-27 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

The invention as recited in claims 1-27 are not clearly understood because of the following reasons:

1) The protect circuit 149, the Low Vcc 125 and the Latch 127 in Fig. 1A and the control circuit 340 in Fig. 32 are not described in the specification;

2) All elements and numerical labels in Figs. 1B-C and 2-31 are not completely described in the specification and the description for these Figs. 1B-C

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and 2-31 in the specification are not consistently read on the corresponding elements and numerical labels in Figs. 1B-C and 2-31. For examples, only elements 150, 152, 154 and pins 1-54 in Fig. 1B are described in the specification, all the remaining elements are not seen in the specification. Similarly, only element 160 in Fig. 1C is described in the specification, all the remaining ones are not seen in the specification.

3) The connective relationship of the control circuit 340 in Fig. 32 is not defined since Fig. 32 does not show the control circuit 340 having any connection to the other circuits in Fig. 32;

4) The limitations as recited in claims 16-17, 20, 23 and 25-26 are not understood since they are not seen in the drawings of the present invention.

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

5. Claims 5 and 8 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 5, it is not clear whether "the next clock cycle" (line 3) is the same as the next clock cycle in the data read operation as recited in claim 1.

Claim 8 no antecedent basis for "the first clock signal".

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Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

7. Claims 1-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Patel, 5,539,696.

Patel, 5,539,696, discloses in Fig. 1 a synchronous memory system 10 which can be applied to DRAM, SRAM or nonvolatile EEPROM, PROM, ROM devices (see lines 22-25, column 4) comprising: digital processor 12; synchronous memory cell array 14; address bus 16; data bus 18; control bus 20; I/O circuit 26 and system clock 22; as shown in Fig. 2, synchronous memory cell array 14 comprising: memory cell bank A, memory cell bank B, timing and control circuit 29 including mode register 29; row address buffer 38, row decoders 40 and 42, column address buffer 44, column decoders 58, 60, 62, and 64; write latches 102, 104, 106, 108 and 110 as shown in Fig. 3; wherein: data can be read out or written into memory cell bank A and memory cell bank B independently every clock cycle in burst mode depending upon the BANK SELECT

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signal generated by timing and control circuit 28 (see the summary of the invention, lines 32-67, column 5, lines 1-44, column 6 and lines 50-64, column 7); therefore, the above synchronous memory system 10 obviously can be capable to perform a data writing operation on memory cell bank A on a first clock cycle and perform a data read operation on memory cell bank B on the next clock cycle or vice versa depending upon the desired programming for the timing and control circuit 28.

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Sakurai, 6,064,627, Jyouno et al., 5,892,713, Norman, 6,134,143, Matsui, 5,953,280, and Jung et al., 6,067,255.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to TRONG PHAN whose telephone number is (703) 308-4870 and email address is trong.phan@uspto.gov



**TRONG PHAN
PRIMARY EXAMINER**

January 5, 2002